

**REMARKS**

Claims 22-35 are pending in this application. By this Amendment, claims 22, 27, 28 and 34 are amended. Reconsideration based on the above amendments and following remarks is respectfully requested.

This Amendment should be entered after final rejection because the amendments: (a) place the application in condition for allowance (for the reasons discussed herein); (b) do not raise any new issue requiring further search and/or consideration (since the amendments amplify issues previously discussed throughout prosecution); (c) satisfy a requirement of form asserted in the previous Office Action; (d) do not present any additional claims without canceling a corresponding number of finally rejected claims; and (e) place the application in better form for appeal, should an appeal be necessary. The amendments are necessary and were not earlier presented because they are made in response to arguments raised in the final rejection. Entry of the amendments is thus respectfully requested.

The Office Action rejects claims 22-24 and 27-29 under 35 U.S.C. §102(b) over U.S. Patent No. 5,412,397 to Kanatani et al. (hereinafter "Kanatani"); claims 25, 26, 30 and 33 under 35 U.S.C. §103(a) over Kanatani in view of U.S. Patent No. 4,816,816 to Usui (hereinafter "Usui"); and claims 31, 32, 34 and 35 under 35 U.S.C. §103(a) over Kanatani in view of Usui and further in view of U.S. Patent No. 5,856,816 to Youn (hereinafter "Youn") and European Patent Application No. EP 0 678 848 to Maekawa (hereinafter "Maekawa"). The rejections are respectfully traversed.

Kanatani, alone or in combination with Usui, Youn or Maekawa, fails to disclose, teach or suggest a display device (or a display substrate) comprising, inter alia, a first data line driving circuit and a second data line driving circuit, the first data line driving circuit being a line sequential driver, the second data line driving circuit having a shift register and a plurality of switches, each switch connected to the other end of one of the plurality of data

lines, the shift register generating a plurality of single pulses sequentially, each of the switches being turned on in response to a single pulse, as recited in independent claims 22, 27 and 28. Support for the newly recited features is found in the specification at least at page 4, lines 3-5, page 5, line 23 to page 6, line 8, page 13, lines 5-25, and page 25, lines 12-26.

Kanatani, at col. 4, line 45 to col. 5, line 25, and in Fig. 1, discloses a first data line driver 3 and a second data line driver 4 being identical.

Unlike Kanatani, Applicant's invention set forth in claims 22-35 is directed to a display device (or display substrate) that comprises two data line driving circuits, the first data line driving circuit being a line sequential driver that outputs a plurality of pulses to connected data lines at the same time, while the second data line driving circuit outputs a single pulse to each one of the plurality of switches.

Neither Usui, Youn or Maekawa provide the deficiencies in Kanatani discussed above.

Applicant respectfully submits that independent claims 22, 27 and 28 are patentable over the applied art. Claims 23-26 and 29-35, which depend from claims 22, 27 and 28, respectively, also are patentable over the applied art for at least the reasons discussed above. Withdrawal of the rejections of claims 22-24 and 27-29 under 35 U.S.C. §102(b) and claims 25, 26 and 30-35 under 35 U.S.C. §103(a) is respectfully requested.

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of claims 22-35 are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



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